

LCD Module

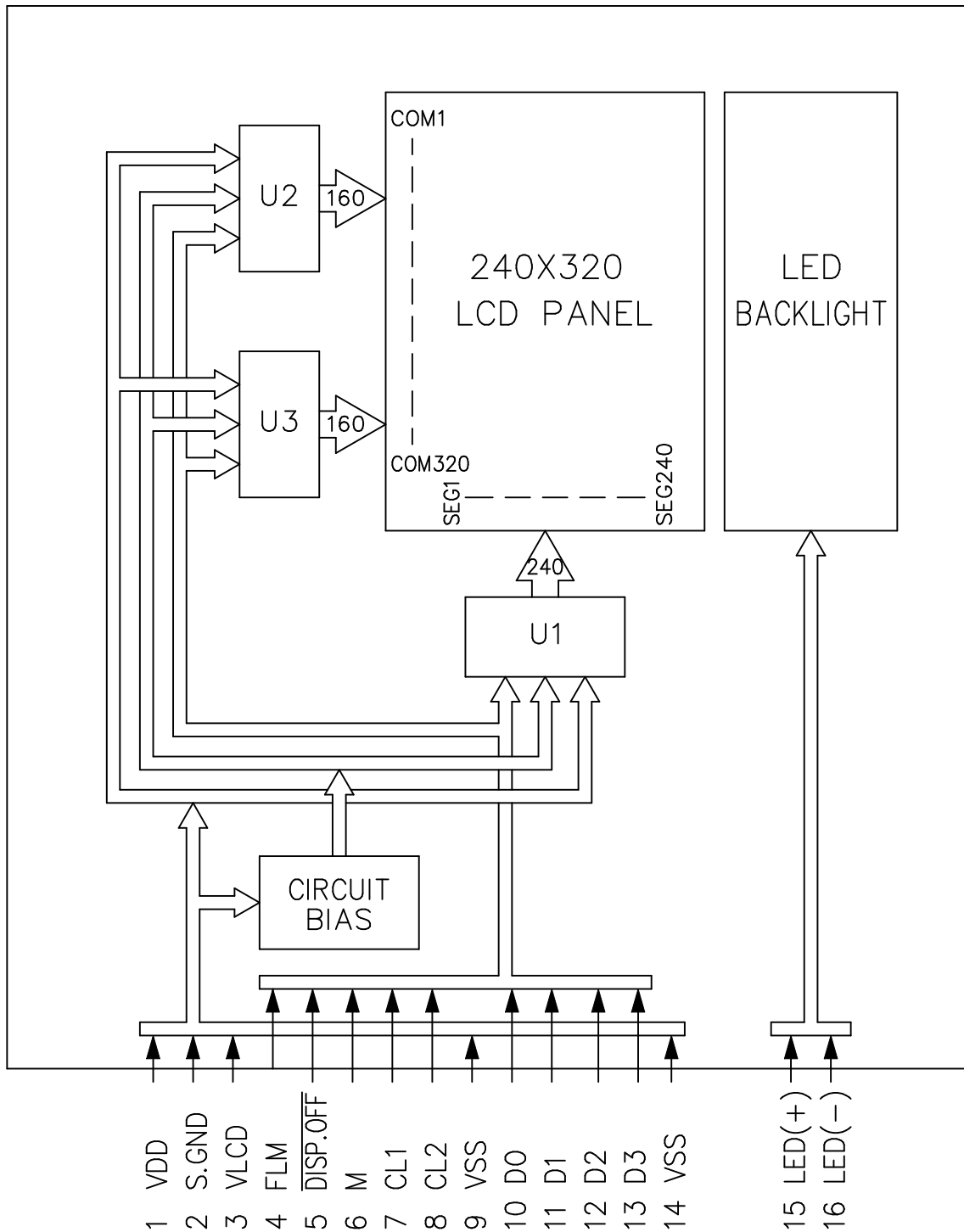
NLC320A240CSK52K

RoHS

(Status: Juni 2006, Rev. 0)



5. BLOCK DIAGRAM



SPECIFICATION

DATE : Jun. 21. 2006

SHEET NO. : 9/24

6.INTERNAL PIN CONNECTION

FPC ,20 pins,pitch 0.5mm

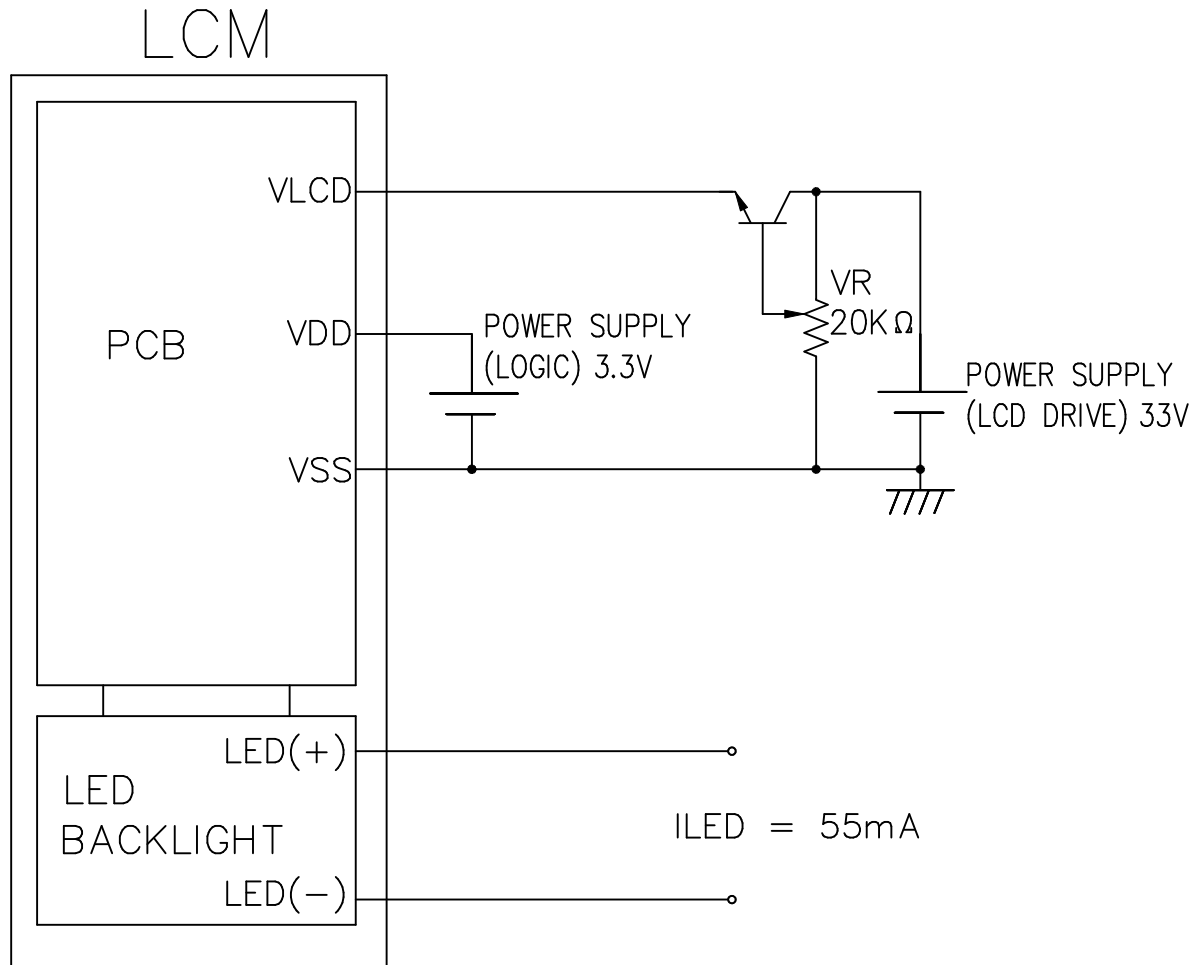
Pin No.	Symbol	Function
1	VDD	POWER SUPPLY FOR LOGIC
2	S.GND	SHIELD GROUND
3	VLCD	POWER SUPPLY FOR LCD
4	FLM	FIRST LINE MARKER
5	DISP.OFF	H: ON/L: OFF
6	M	SWITCH SIGNAL TO CONVERT LIQUID CRYSTAL DRIVE WAVEFORM INTO AC
7	CL1	DATA LATCH
8	CL2	SHIFT CLOCK
9	VSS	LOGIC GROUND
10	D0	DISPLAY DATA
11	D1	DISPLAY DATA
12	D2	DISPLAY DATA
13	D3	DISPLAY DATA
14	VSS	LOGIC GROUND
15	LED(+)	POWER SUPPLY FOR LED
16	LED(-)	POWER SUPPLY FOR LED
17	NC	NC
18	NC	NC
19	NC	NC
20	NC	NC

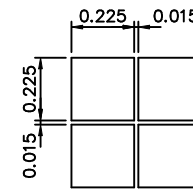
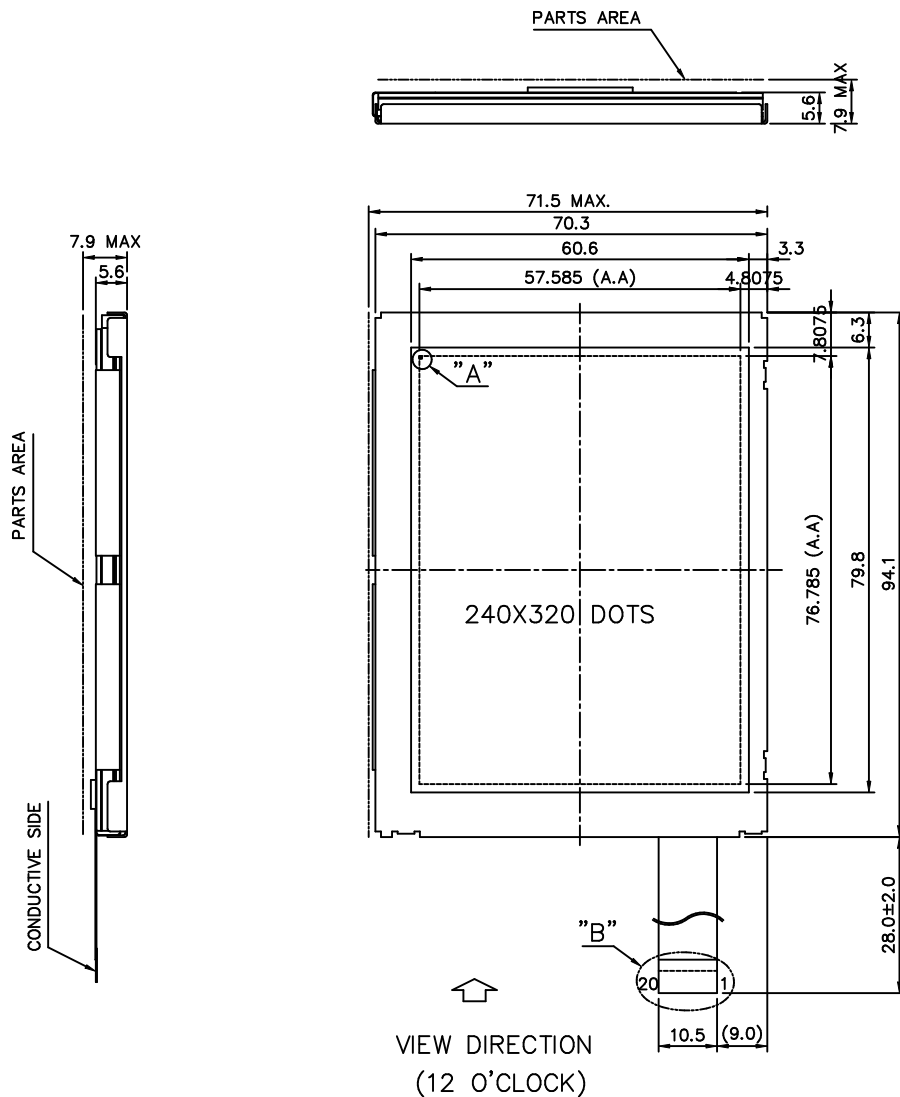
Mating Connector:MOLEX 52746-2070

REV/DATE

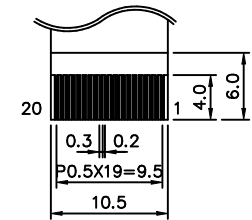
R0/
06.21.06'BY
W.R.HSU

7. POWER SUPPLY





DETAIL "A"



DETAIL "B"

FIRST DATA	Seg 1	Seg 2	Seg 3	Seg 4	Seg 240
Com 1	1.1	1.2	1.3	1.4	1.240
Com 2	2.1	2.2	2.3		
Com 3	3.1	3.2			
D0:(1.4),(1.8).....(320.240) D1:(1.3),(1.7).....(320.239) D2:(1.2),(1.6).....(320.238) D3:(1.1),(1.5).....(320.237)					
Com 320	320.1				320.240

- NOTES:
- 1.RESOLUTION: 240X320 DOTS
 - 2.BACKLIGHT: LED (WHITE)
 - 3.FRAME MATERIAL: SECC
 - 4.GLASS THICKNESS: 0.7 mm

GENERAL TOLERANCE LIST

DIMENSION	TOLERANCE
$L \leq 6$	± 0.25 (mm)
$6 < L \leq 18$	± 0.3 (mm)
$18 < L \leq 50$	± 0.4 (mm)
$50 < L \leq 125$	± 0.5 (mm)
$125 < L$	± 0.6 (mm)
ANGLE	$\pm 1^{\circ}$ (DEG)

NLC320A240CSK52K

REV. NO.	DESCRIPTION	DATE	DESIGN	CHECK	APPROVE	NAME	DATE	THIRD ANGLE P
						APPROVE		
						CHECK		
						DESIGN	CLOUDE	95.05.29
						DRAWN	CLOUDE	95.05.29
								SCALE UNIT
								1/1 mm
						www.admatec.com		

PIN NO	SYMBOL	LEVEL	FUNCTION	PIN NO	SYMBOL	LEVEL	FUNCTION
1	VDD	H	POWER SUPPLY FOR LOGIC	10	DO	H/L	DISPLAY DATA
2	S.GND	-	SHIELD GROUND	11	D1		
3	VLCD	H	POWER SUPPLY FOR LCD	12	D2		
4	FLM	H	FIRST LINE MARKER	13	D3		
5	DISP.OFF	H/L	H: ON/L: OFF	14	VSS	-	LOGIC GROUND
6	M	H/L	SWITCH SIGNAL TO CONVERT LIQUID CRYSTAL DRIVE WAVEFORM INTO AC	15	LED(+)	-	POWER SUPPLY FOR LED
7	CL1	H→L	DATA LATCH	16	LED(-)	-	POWER SUPPLY FOR LED
8	CL2	H→L	SHIFT CLOCK	17			
9	VSS	-	LOGIC GROUND	18			
				19	NC	-	NC
				20			