

# TFT Module

T035C003200240T0CG0B16



# admatec

The better Display Solution

# 1. General description

## 1.1 Introduction

T035C003200240T0CG0B16 is a color active matrix thin film transistor (TFT) liquid crystal display (LCD) that uses amorphous silicon TFT as a switching device. This model is composed of a TFT LCD panel, a driving circuit and a back light system. This TFT LCD has a 3.5 (4:3) inch diagonally measured active display area with QVGA(320 horizontal by 240 vertical pixel) resolution.

## 1.2 Features

3.5 (4:3 diagonal) inch configuration

8 bits + FRC driver with 1channel TTL interface

LED Backlight

RoHS Compliance

## 1.3 Applications

GPS

Personal Navigation Device

Multimedia applications and Others AV system

## 1.4 General information

Item		Specification	Unit
Outline Dimension		76.9 x 63.9 x 3.3 (Typ.)	mm
Display area		70.08(H) x 52.56(V)	mm
Number of Pixel		320 RGB(H) x 240(V)	pixels
Pixel pitch		0.219(H) x 0.219(V)	mm
Pixel arrangement		RGB Vertical stripe	
Display mode		Normally white	
Surface treatment		Antiglare, Hard-Coating(3H)	
Weight		30 (Typ.)	g
Back-light		Single LED (Side-Light type)	
Power Consumption	B/L System	0.5(Max.)	w

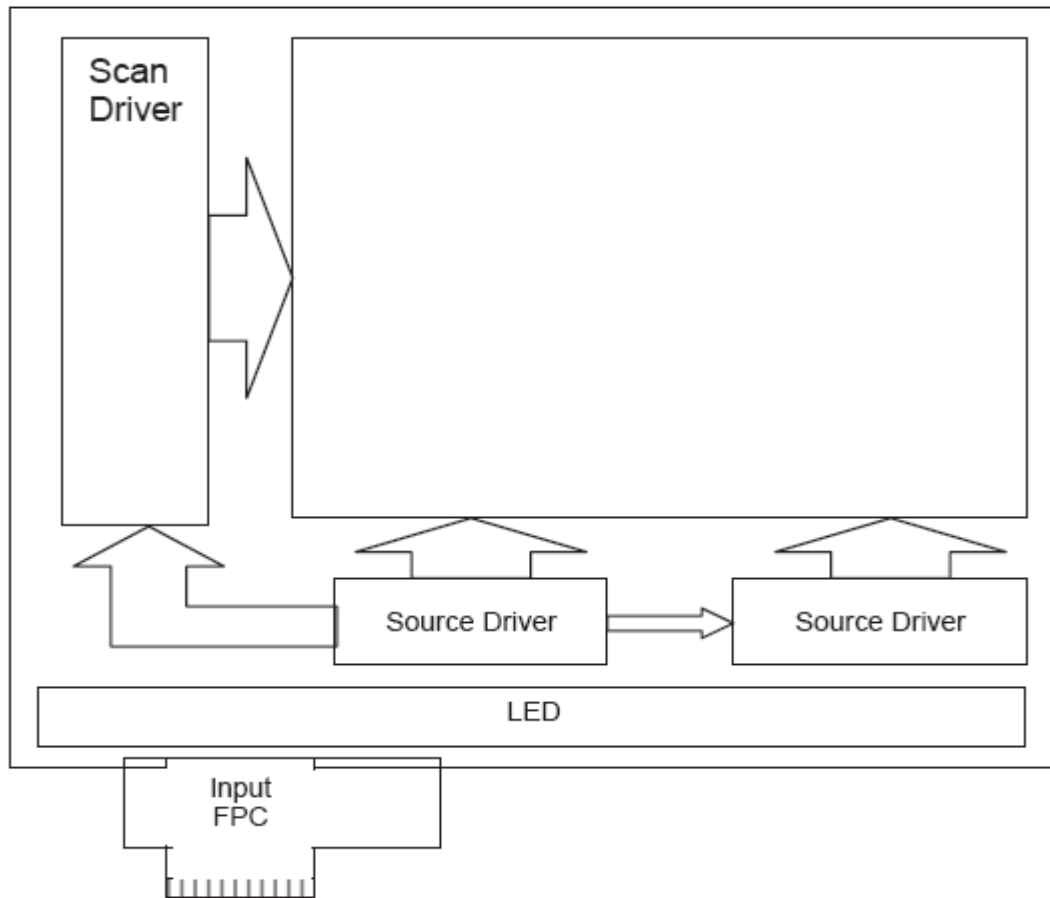
## 1.5 Mechanical Information

item		Min.	Typ.	Max.	Unit
Module Size	Horizontal(H)	76.7	76.9	77.1	mm
	Vertical(V)	63.7	63.9	64.1	mm
	Depth(D)	3.1	3.3	3.5	mm

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## 4.0 BLOCK DIAGRAM

### 4.1 TFT LCD Module



## 5.0 INTERFACE PIN CONNECTION

5.1 TFT LCD ModuleCN2 (Input signal): FPC Down Connector, (FH19SC-54S-0.5SH (HIROSE), 54pin,pitch = 0.5mm)

Terminal No.	Symbol	IO	Functions
1	VLED-	P	Power for LED backlight cathode
2	VLED-	P	Power for LED backlight cathode
3	VLED+	P	Power for LED backlight anode
4	VLED+	P	Power for LED backlight anode
5	NC	I	No connection
6	NC	I	No connection
7	/RESET	I	Reset signal ,low active
8	/CS	I	Chip select signal ,low active
9	SCK	I	Clock signal for SPI
10	SDI	I	Data Input for SPI
11	SDO	O	Data Output for SPI
12	B0	I	Data Input (LSB)
13	B1	I	Data Input
14	B2	I	Data Input
15	B3	I	Data Input
16	B4	I	Data Input
17	B5	I	Data Input
18	B6	I	Data Input
19	B7	I	Data Input(MSB)
20	G0	I	Data Input (LSB)
21	G1	I	Data Input
22	G2	I	Data Input
23	G3	I	Data Input
24	G4	I	Data Input
25	G5	I	Data Input
26	G6	I	Data Input
27	G7	I	Data Input(MSB)
28	R0	I	Data Input (LSB)
29	R1	I	Data Input
30	R2	I	Data Input
31	R3	I	Data Input

**SPECIFICATION**

**Jun 14, 2016**

**12(20)**

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**V02**

32	R4	I	Data Input
33	R5	I	Data Input
34	R6	I	Data Input
35	R7	-	Data Input(MSB)
36	HSYNC	P	Horizontal sync Signal
37	VSYNC	I/O	Vertical sync signal
38	DCLK	I/O	Pixel clock signal
39	VDDIO	I/O	Power Voltage typal 3.3V
40	VDDIO	I/O	Power Voltage typal 3.3V
41	VCI	I	Power Voltage typal 3.3V
42	VCI	I	Power Voltage typal 3.3V
43	REGVDD	I	Power Voltage typal 3.3V
44	NC	-	No connection
45	NC	P	No connection
46	NC	I/O	No connection
47	NC	I/O	No connection
48	NC	I/O	No connection
49	NC	I/O	No connection
50	NC	I	No connection
51	NC	I	No connection
52	DE	I	Data Enable when DE mode,leave it float if sync mode
53	GND	-	Power Ground
54	GND	P	Power Ground

