



# LCD Module

## T070C010240600T20G0B18

# DATA SHEET

## Product specification V 1.1

### Approval of Specification

	Approved by	Date
<b>admatec</b>	Dipl.-Ing. H. Gharbi	2016-03-11
<b>Customer</b>		

This product complies to EU directive 2002/95/EC (RoHS) of January 27<sup>th</sup>, 2003.

# 1 General data

**Table 1.1: General data**

No.	Item	Content	
1.	module size	180.2 mm (W) * 112.00 mm (H) * 5.42 mm (T)	
2.	visible area	155.3 mm (W) * 87.1 mm (H)	
3.	active area	154.21 mm (W) * 85.92 mm (H)	
4.	dot pitch	0.151 mm (W) * 0.143 mm (H)	
5.	number of dots	1024 * 3(RGB) (W) * 600 (H)	
6.	LCD type	IPS-TFT, color16.7M, transmissive	
7.	backlight	LED white	
8.	controller	EK79001	
9.	interface	LVDS	
10.	touch panel	type	capacitive (CTP)
		material	glass / glass
		controller	GT911
		interface	I <sup>2</sup> C
11.	weight	215 g (approx.)	

## 5 Block diagram

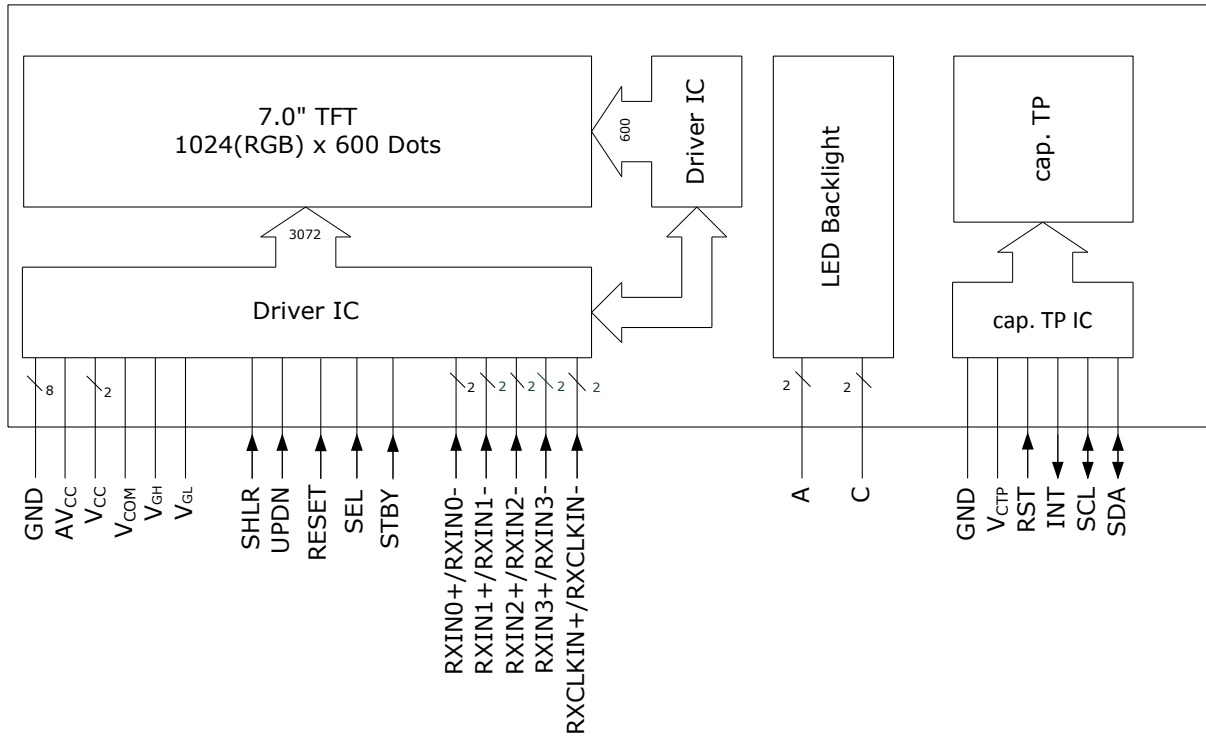


Figure 5.1 : Block diagram of T070C010240600T20G0B18

## 6 Interface description

### 6.1 Interface of LCD

**Table 6.1: LCD interface description**

Symbol	Pin no.	IO	Function
V <sub>COM</sub>	1	PWR	common voltage
V <sub>CC</sub>	2	PWR	power supply for logic
V <sub>CC</sub>	3	PWR	power supply for logic
NC	4	--	not connected
RESET	5	I	reset
STBY	6	I	standby mode 'H' = normal operation    'L' = TFT will turn off
GND	7	PWR	ground
RXIN0-	8	I	negative LVDS differential data inputs 0
RXIN0+	9	I	positive LVDS differential data inputs 0
GND	10	PWR	ground
RXIN1-	11	I	negative LVDS differential data inputs 1
RXIN1+	12	I	positive LVDS differential data inputs 1
GND	13	PWR	ground
RXIN2-	14	I	negative LVDS differential data inputs 2
RXIN2+	15	I	positive LVDS differential data inputs 2
GND	16	PWR	ground
RXCLKIN-	17	I	negative LVDS differential clock inputs
RXCLKIN+	18	I	positive LVDS differential clock inputs
GND	19	PWR	ground
RXIN3-	20	I	negative LVDS differential data inputs 3
RXIN3+	21	I	positive LVDS differential data inputs 3
GND	22	PWR	ground
NC	23	--	not connected
NC	24	--	not connected
GND	25	PWR	ground
NC	26	--	not connected
NC	27	--	not connected
SEL	28	I	6-bit/8-bit mode select 'H' = 6-bit mode    'L' = 8-bit mode
AV <sub>CC</sub>	29	PWR	power supply for LCD
GND	30	PWR	ground
C	31	PWR	backlight cathode
C	32	PWR	backlight cathode
SHLR	33	I	horizontal inversion
UPDN	34	PWR	vertical inversion
V <sub>GL</sub>	35	I	gate off power supply
NC	36	--	not connected
NC	37	--	not connected
V <sub>GH</sub>	38	PWR	gate on power supply
A	39	PWR	backlight anode
A	40	PWR	backlight anode

FPC: 40 pin, 0.5 mm pitch

## 6.2 Interface of CTP

**Table 6.2: CTP interface description**

Symbol	Pin no.	IO	Function
RST	1	I	reset
V <sub>CTP</sub>	2	PWR	power supply for logic
GND	3	PWR	ground
INT	4	O	Interrupt
SDA	5	I/O	serial data
SCL	6	I/O	serial clock
NC	7	--	not connected
NC	8	--	not connected
NC	9	--	not connected
NC	10	--	not connected

# 10 Drawing

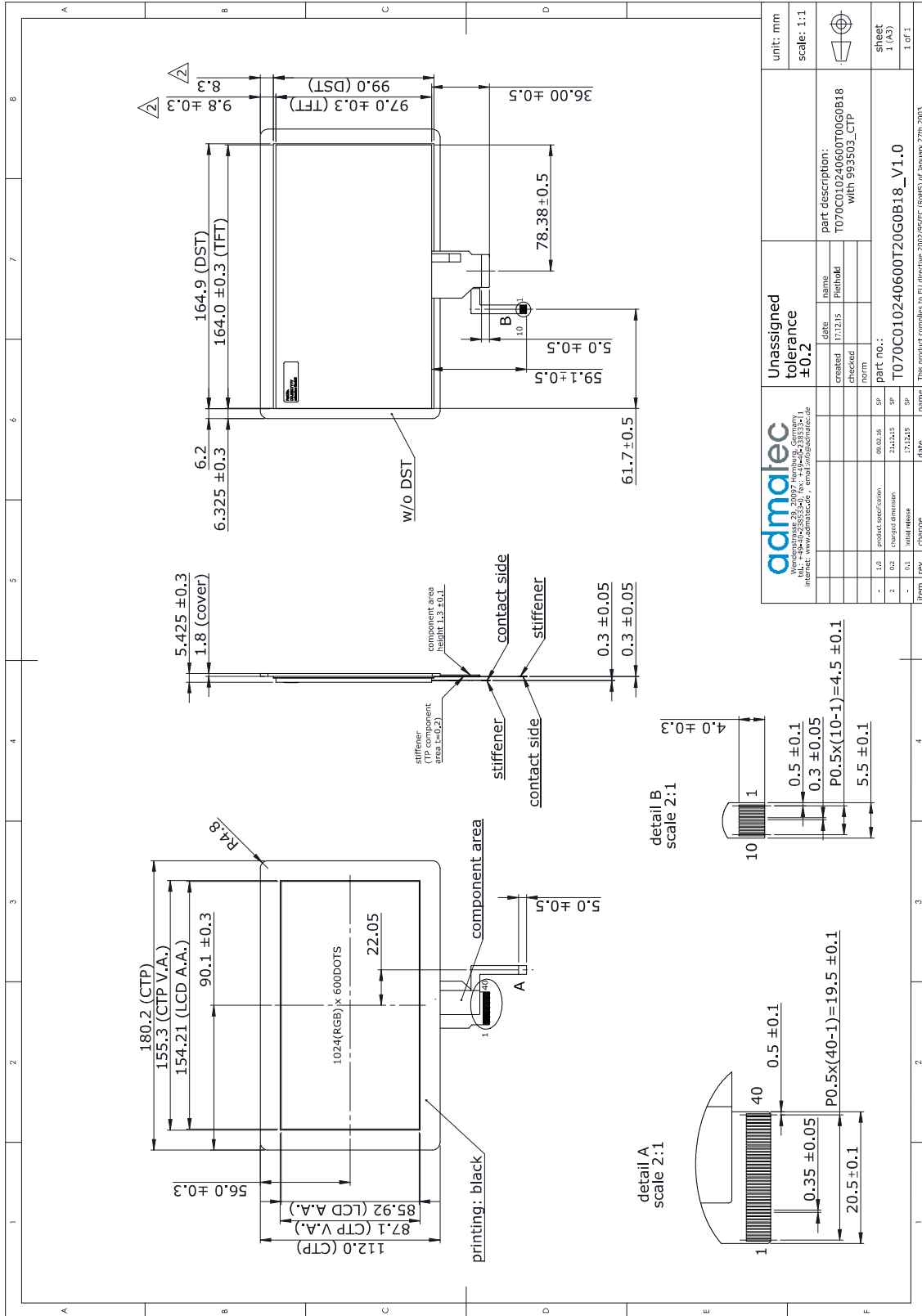


Fig 10.1: Drawing of T070C010240600T20G0B18

admatec		Unassigned tolerance $\pm 0.2$		unit: mm
admatec Tel: +43-1-2852340 Fax: +43-1-2852341 Internet: www.admatec.de, email: info@admaterc.de		created	date	scale: 1:1
checked	name	part description:	part no.:	sheet
norm	Plethold	T070C010240600T0G0B18	T070C010240600T20G0B18_V1.0	1 (A3)
SP	SP	with 993503_CTP		1 of 1
09.02.16	17.12.15			
21.12.15	12.12.15			
12.12.15				
02	change dimension			
01	initial release			
item	rev.	change	date	name
				This product complies to EU directive 2002/95/EC (RoHS) of January 27th, 2003